Process Change Notice #1212072

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PCN Date: 12/7/2012	**Effective Date:** 12/12/2012	
Title: C8051F39x/7x Datasheet v1.0 Availablity		
Originator: Shahram Tadayon	**Phone:** 512 428 1698	**Dept:** Marketing
Customer Contact: Kathy Haggar	**Phone:** 512 532 5261	**Dept:** Sales

PCN Type:

☒ **Datasheet**	☐ **Foundry**	☐ **Packing**
☐ **Product Revision**	☐ **Assembly**	☐ **Labeling**
☐ **Discontinuance**	☐ **Test**	☐ **Other**

Last Order Date: Not Applicable.

PCN Details

Description of Change:
Version 1.0 of the C8051F39x/7x is now available. This update applies to the C8051F39x and C8051F37x family of MCUs.

See Appendix for complete description of the changes.

The C8051F39x/7x datasheet is available at www.silabs.com.
Please contact your Silicon Labs Sales Representative if you have questions. A list of Silicon Labs Sales Representatives is available at www.silabs.com

Reason for Change:
The C8051F39x/7x datasheet includes minor updates, additions and corrections to the specifications and text descriptions, as noted in the Appendix.

Impact on Form, Fit, Function, Quality, Reliability:
There is no impact on form, fit, function, quality or reliability.

Product Identification:
The following orderable part numbers are affected:

- C8051F370-A-GM
- C8051F371-A-GM
- C8051F374-A-GM
- C8051F375-A-GM
- C8051F390-A-GM
- C8051F391-A-GM
- C8051F392-A-GM
- C8051F393-A-GM
- C8051F394-A-GM
- C8051F395-A-GM
- C8051F396-A-GM
- C8051F397-A-GM
- C8051F398-A-GM
- C8051F399-A-GM

Last Date of Unchanged Product: Not Applicable.

Qualification Samples:

Samples available upon request.

Customer Early Acceptance Sign Off:

Customers may approve early PCN acceptance by completing the information below:

Early Acceptance: **Date:** _____

Name: _____

Company: _____

Email your early Acceptance approval to: katherine.haggar@silabs.com

Qualification Data:

Qualification data is available upon request. Please contact your sales representative for a copy of the Qualification data. A list of Silicon Labs' Sales Representatives is available at www.silabs.com

Appendix

1. Typographical and content corrections and clarification throughout.
2. Electrical Specifications Tables Additions:
   - Typical precision temperature sensor curve Figure 7.3
3. Electrical Specifications Tables Removals:
   - None
4. Electrical Specifications Tables Corrections/Adjustments:
   - C mode K factors in the OSCXCN SFR definition.
   - IDD normal mode, max @ 50 MHz, 3.6 V = 7.8 mA
   - IDD normal mode, max @ 50 MHz, 3.0 V = 7.7 mA
   - IDD normal mode, max @ 25 MHz, 3.6 V = 5.2 mA
   - IDD normal mode, max @ 25 MHz, 3.0 V = 5.1 mA
   - Digital supply current, suspend mode, typ = 73 µA
   - Digital supply current, stop mode, typ = 75 µA
   - Precision temperature sensor absolute error, typ = 0.2 °C
   - precision temperature sensor INL, max = ±0.4 °C
   - IDAC DNL @ –40 to 0°C, min = -1.0 LSB
   - IDAC full scale error @ 2mA full scale output, min = -122 µA, max = 40 µA
   - IDAC full scale error @ 1mA full scale output, min = -61 µA, max = 20 µA
   - IDAC full scale error @ 0.5mA full scale output, min = -31 µA, max = 10 µA
   - IDAC gain variation @ 1 mA full scale output, typ = 0.1 %
   - IDAC gain variation @ 0.5 mA full scale output, typ = 0.1 %
   - Comparator supply current at DC, mode 0, max = 50 µA
   - Comparator supply current at DC, mode 1, max = 25 µA
   - Comparator supply current at DC, mode 2, max = 12 µA
5. Pin Definitions and Packaging Information:
   . None
6. Flash Security Changes:
   - Erase page containing lock byte (if no pages are locked), firmware executing from unlocked page = permitted
   - Erase page containing lock byte (if no pages are locked), firmware executing from locked page = N/A

